

LSBs OF  
THE BYTE  
ADDRESS

FIG. 3A

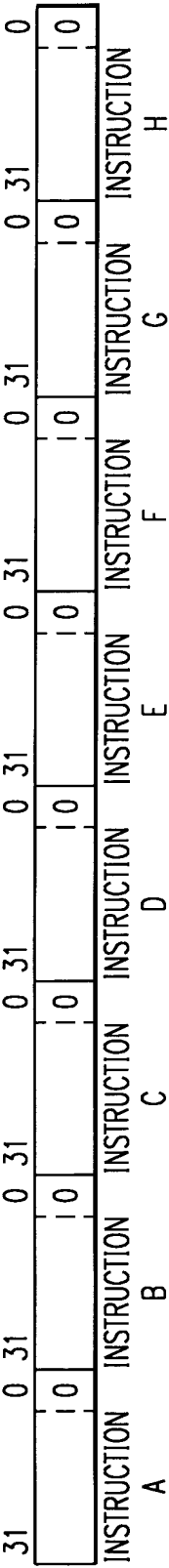


FIG. 3B

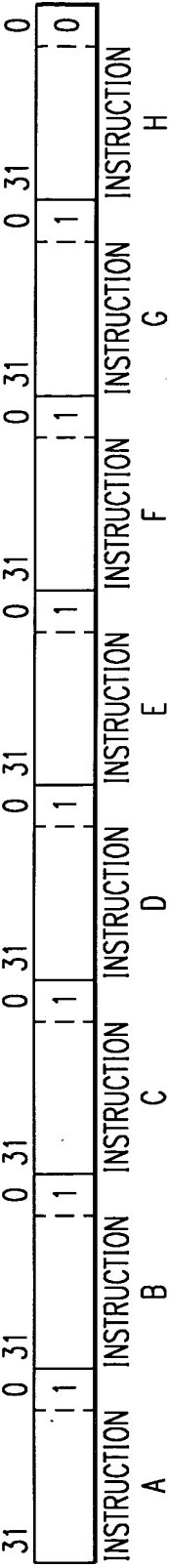


FIG. 3C

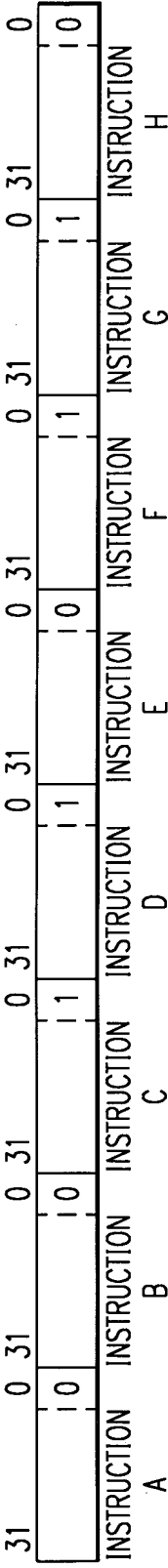
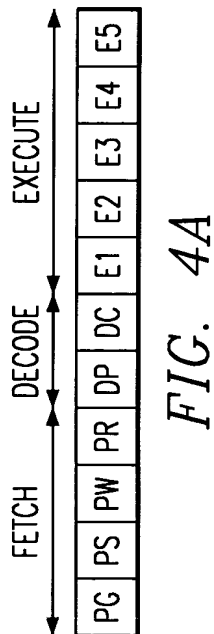
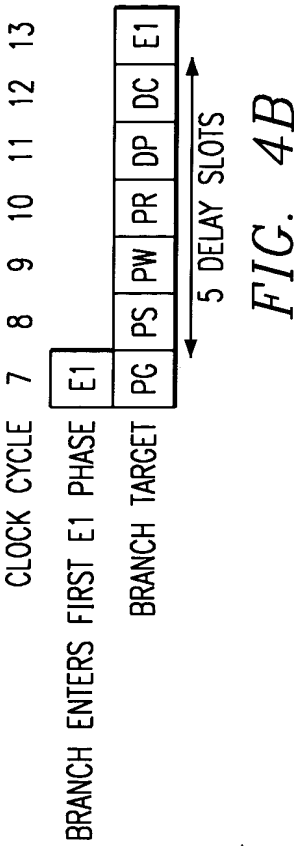
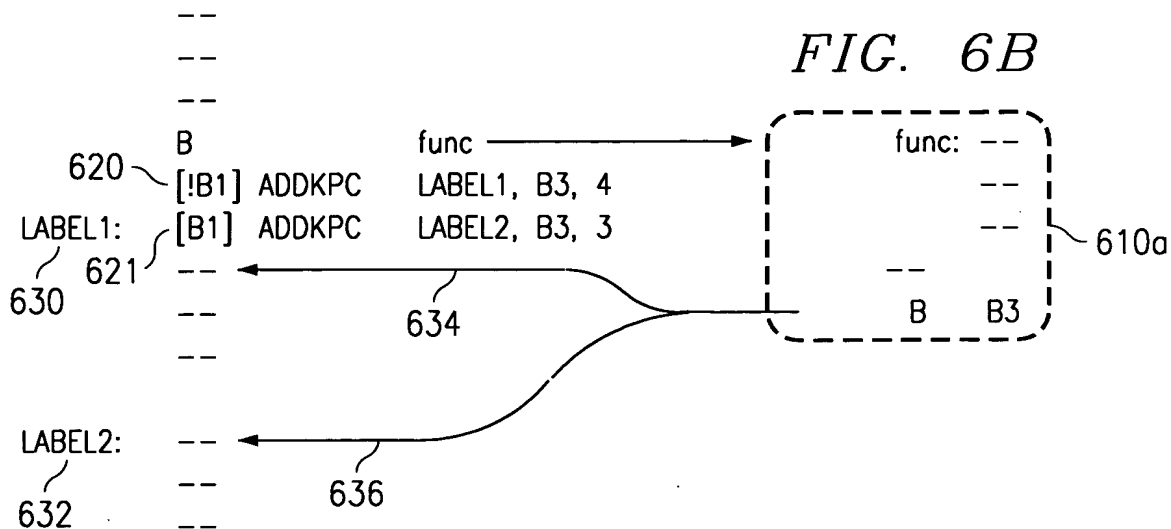
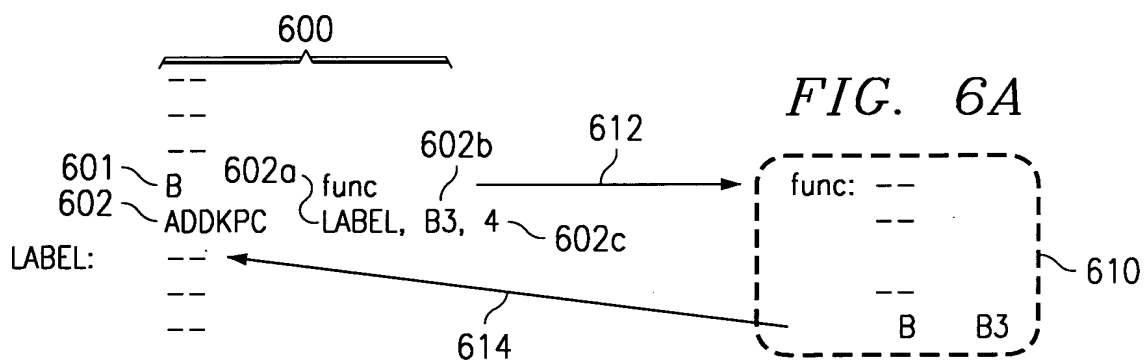
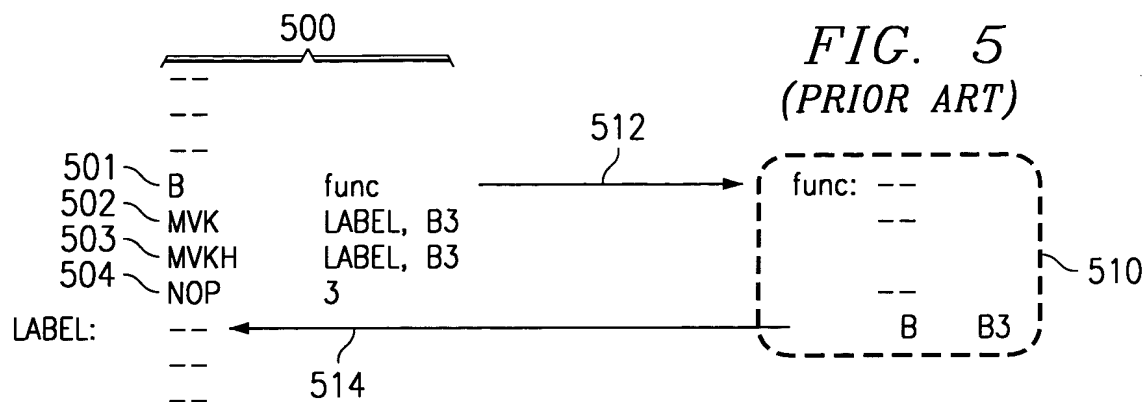


FIG. 3D



	CLOCK CYCLE																						
FETCH PACKET	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
n	PG	PS	PW	PR	DP	DC	E1	E2	E3	E4	E5												
k																							
n+1																							
n+2																							
n+3																							
n+4																							
n+5																							
n+6																							

FIG. 4C



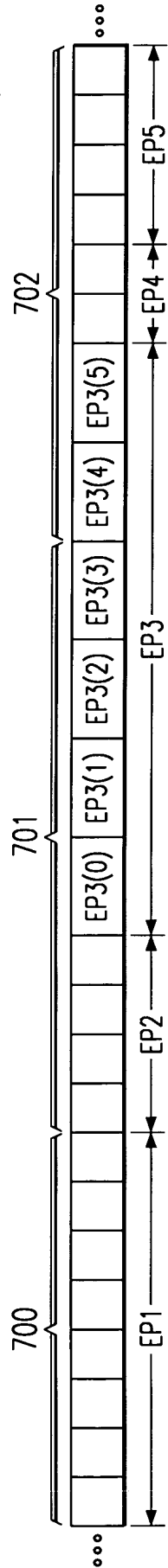


FIG. 7

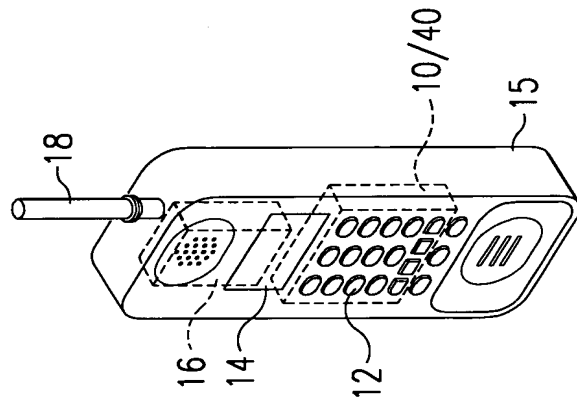


FIG. 11





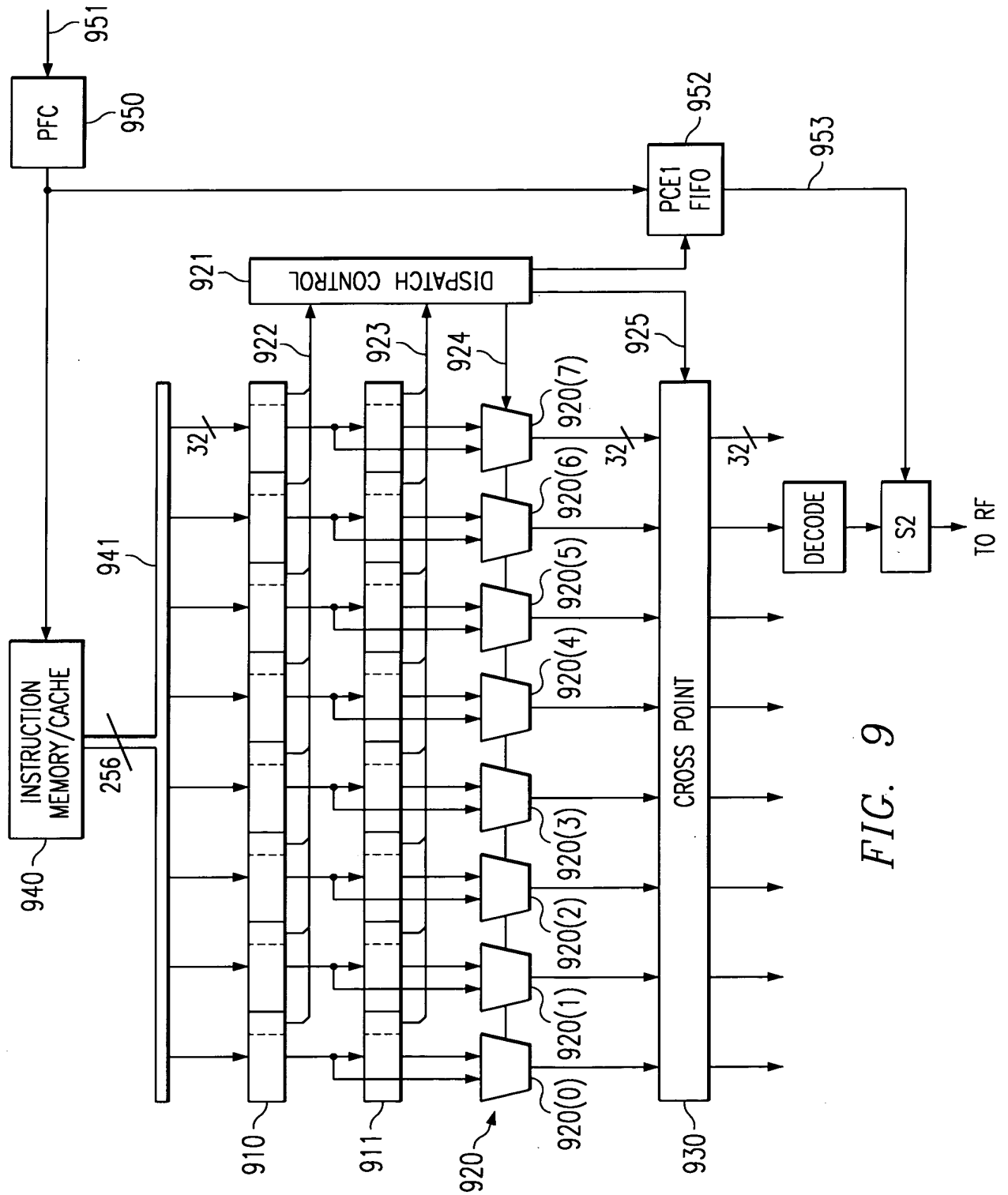


FIG. 9

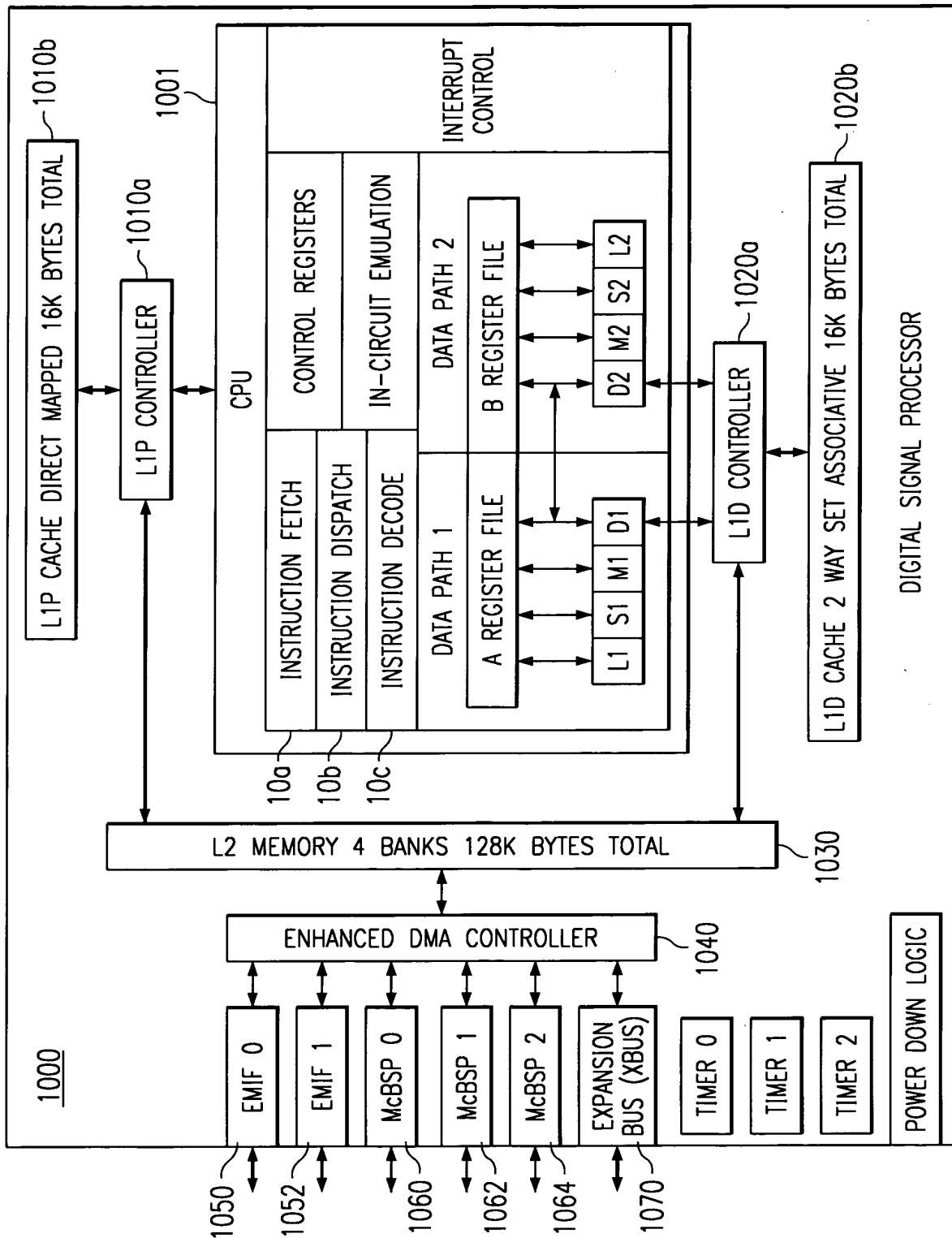


FIG. 10